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# **Application Data Sheet**

	Ap	plication	Inform	ation
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Application Type::

Regular

Subject Matter::

Utility

Suggested Classification::

Suggested Group Art Unit::

CD-ROM or CD\_R?::

None

Number of CD disks::

Number of copies of CDs::

Sequence Submission::

No

Computer Readable Form (CRF)?::

No

Title::

CHIP RESISTOR HAVING LOW RESISTANCE AND

METHOD OF MAKING THE SAME

Attorney Docket Number::

10921.261USWO

Request For Early Publication::

No

Request For Non-Publication::

No

Suggested Drawing Figure::

**Total Drawing Sheets::** 

12

Small Entity::

No

Latin Name::

Variety Denomination Name::

Petition Included::

No

Petition Type::

Licensed US Govt. Agency::

Contract or Grant Numbers::

Secrecy Order in Parent Appl.?::

No

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#### **Applicant Information**

**Applicant Authority Type::** 

Inventor

Primary Citizenship Country::

Japan

Status::

Full Capacity

Given Name::

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Middle Name::

Family Name::

**TSUKADA** 

Name Suffix::

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State or Province of Residence::

**Kyoto** 

Country of Residence::

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Street of mailing address::

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City of mailing address::

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State or Province of mailing address::

**Kyoto** 

Country of mailing address::

Japan

Postal or Zip Code of mailing address:: 615-8585

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### Correspondence Information

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Correspondence Customer Number:: 23552

#### Representative Information

Representative Customer Number::	23552

#### **Domestic Priority Information**

Application::	Continuation Type::	Parent Application::	Parent Filing Date::
This application	National Stage of	PCT/JP2003/007457	June 12, 2003

### Foreign Priority Information

Country::	Application Number::	Filing Date::	Priority Claimed::
Japan	2002-177970	June 19, 2002	Yes
Japan	2002-177971	June 19, 2002	Yes

## **Assignee Information**

Assignee Name::

ROHM CO., LTD.

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21 Saiin Mizosaki-cho, Ukyo-ku

City of mailing address::

Kyoto-shi

State or Province of mailing address::

**Kyoto** 

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